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PLUS Search Results for S/N 10029826, Searched June 17, 2004

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most closely related linguistically to the application searched. This search was prepared by the staff of the Scientific and Technical Information Center, SIRA.

6748559
6125407
4926375
6023732
4858112
4789982
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10029826_LIST

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5754887
5768617
5826107
5898694
5954819
5978938
5983355

10029826_QUAL

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| 6748559 | 74 |
| 6125407 | 72 |
| 4926375 | 72 |
| 6023732 | 69 |
| 4858112 | 66 |
| 4789982 | 66 |
| 5048014 | 66 |
| 4506360 | 65 |
| 5857114 | 65 |
| 5594875 | 63 |
| 5682551 | 63 |
| 4517644 | 63 |
| 6216189 | 63 |
| 6216189 | 63 |
| 5619687 | 63 |
| 5983356 | 63 |
| 4495564 | 62 |
| 5228066 | 62 |
| 5535400 | 62 |
| 5604803 | 61 |
| 5732137 | 61 |
| 6002737 | 61 |
| 5524147 | 61 |
| 5506963 | 60 |
| 4488232 | 60 |
| 4575847 | 60 |
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| 4807118 | 59 |
| 4799215 | 59 |
| 5884160 | 59 |
| 6049825 | 59 |
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| 5898694 | 58 |
| 5954819 | 58 |

10029826_QUAL

5978938 58
5983355 58

10029826_CLS
Most Frequently Occurring Classifications of Patents Returned
From A Search of 10029826 on June 17, 2004

Original Classifications

3 713/322
2 377/20
2 710/107
2 710/113
2 713/155
2 719/312

Cross-Reference Classifications

3 370/452
3 710/22
3 713/320
3 714/43
2 377/16
2 709/220
2 710/107
2 710/11
2 710/58
2 710/7
2 711/100
2 711/4
2 713/152
2 714/4

Combined Classifications

4 710/107
4 710/22
3 370/452
3 710/113
3 710/58
3 710/7
3 713/320
3 713/322
3 714/43
2 377/16
2 377/20
2 709/220
2 709/237
2 709/253
2 710/11
2 710/36
2 710/4
2 711/100
2 711/4

10029826_CLS

2 713/152
2 713/155
2 713/502
2 714/4
2 714/56
2 719/312

10029826_CLSTITLES

Titles of Most Frequently Occurring Classifications of Patents Returned

From A Search of 10029826 on June 17, 2004

| | | |
|---|-------------|--|
| 4 | 710/107 | (2 OR, 2 XR) |
| | Class 710 : | ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT |
| | 710/100 | INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING) |
| | 710/107 | .Bus access regulation |
| 4 | 710/22 | (1 OR, 3 XR) |
| | Class 710 : | ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT |
| | 710/1 | INPUT/OUTPUT DATA PROCESSING |
| | 710/22 | .Direct Memory Accessing (DMA) |
| 3 | 370/452 | (0 OR, 3 XR) |
| | Class 370 : | MULTIPLEX COMMUNICATIONS |
| | 370/431 | CHANNEL ASSIGNMENT TECHNIQUES |
| | 370/449 | .Polling |
| | 370/450 | ..Passing a signal identifying the idle or bus |
| | | state of a channel (e.g., token passing) |
| | 370/451 | ...On bus |
| | 370/452 |On ring or loop |
| 3 | 710/113 | (2 OR, 1 XR) |
| | Class 710 : | ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT |
| | 710/100 | INTRASYSTEM CONNECTION (E.G., BUS AND BUS TRANSACTION PROCESSING) |
| | 710/107 | .Bus access regulation |
| | 710/113 | ..Centralized bus arbitration |
| 3 | 710/58 | (1 OR, 2 XR) |
| | Class 710 : | ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT |
| | 710/1 | INPUT/OUTPUT DATA PROCESSING |
| | 710/58 | .Input/Output process timing |
| 3 | 710/7 | (1 OR, 2 XR) |
| | Class 710 : | ELECTRICAL COMPUTERS AND DIGITAL DATA PROCESSING SYSTEMS: INPUT/OUTPUT |
| | 710/1 | INPUT/OUTPUT DATA PROCESSING |
| | 710/5 | .Input/Output command process |
| | 710/7 | ..Concurrently performing Input/Output |

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operation and other operation unrelated to

Input/Output

- 3 713/320 (0 OR, 3 XR)
 Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: SUPPORT
 713/300 COMPUTER POWER CONTROL
 713/320 .Power conservation
- 3 713/322 (3 OR, 0 XR)
 Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: SUPPORT
 713/300 COMPUTER POWER CONTROL
 713/320 .Power conservation
 713/322 ..By clock speed control (e.g., clock on/off)
- 3 714/43 (0 OR, 3 XR)
 Class 714 : ERROR DETECTION/CORRECTION AND FAULT
 DETECTION/RECOVERY
 714/100 DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING
 714/1 .Reliability and availability
 714/25 ..Fault locating (i.e., diagnosis or testing)
 714/40 ...Component dependent technique
 714/43Bus, I/O channel, or network path componen
 t
 fault
- 2 377/16 (0 OR, 2 XR)
 Class 377 : ELECTRICAL PULSE COUNTERS, PULSE DIVIDERS, OR
 SHIFT REGISTERS: CIRCUITS AND SYSTEMS
 377/1 APPLICATIONS
 377/16 .Determining machine or apparatus operating
 time or monitoring machine, apparatus or op
 eration
- 2 377/20 (2 OR, 0 XR)
 Class 377 : ELECTRICAL PULSE COUNTERS, PULSE DIVIDERS, OR
 SHIFT REGISTERS: CIRCUITS AND SYSTEMS
 377/1 APPLICATIONS
 377/19 .Measuring or testing
 377/20 ..Time combined with measurement of another
 parameter
- 2 709/220 (0 OR, 2 XR)
 Class 709 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING

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SYSTEMS: MULTIPLE COMPUTER OR PROCESS COO

RDINATING

709/200 MULTICOMPUTER DATA TRANSFERRING
 709/220 .Network computer configuring

2 709/237 (1 OR, 1 XR)

Class 709 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: MULTIPLE COMPUTER OR PROCESS COO

RDINATING

709/200 MULTICOMPUTER DATA TRANSFERRING
 709/230 .Computer-to-computer protocol implementing
 709/237 ..Computer-to-computer handshaking

2 709/253 (1 OR, 1 XR)

Class 709 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: MULTIPLE COMPUTER OR PROCESS COO

RDINATING

709/200 MULTICOMPUTER DATA TRANSFERRING
 709/253 .Bused computer networking

2 710/11 (0 OR, 2 XR)

Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
 PROCESSING SYSTEMS: INPUT/OUTPUT
 710/1 INPUT/OUTPUT DATA PROCESSING
 710/8 .Peripheral configuration
 710/11 ..Protocol selection

2 710/36 (1 OR, 1 XR)

Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
 PROCESSING SYSTEMS: INPUT/OUTPUT
 710/1 INPUT/OUTPUT DATA PROCESSING
 710/36 .Input/Output access regulation

2 710/4 (1 OR, 1 XR)

Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA
 PROCESSING SYSTEMS: INPUT/OUTPUT
 710/1 INPUT/OUTPUT DATA PROCESSING
 710/3 .Input/Output addressing
 710/4 ..Address data transfer

2 711/100 (0 OR, 2 XR)

Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: MEMORY
 711/100 STORAGE ACCESSING AND CONTROL

2 711/4 (0 OR, 2 XR)

Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
 SYSTEMS: MEMORY

10029826 CLSTITLES

711/1 ADDRESSING COMBINED WITH SPECIFIC MEMORY
CONFIGURATION OR SYSTEM

711/4 .Dynamic-type storage device (e.g., disk, tape
drum)

2 713/152 (0 OR, 2 XR)
Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: SUPPORT

713/150 MULTIPLE COMPUTER COMMUNICATION USING
CRYPTOGRAPHY

713/151 .Protection at a particular protocol layer

713/152 ..Application layer security

2 713/155 (2 OR, 0 XR)
Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: SUPPORT

713/150 MULTIPLE COMPUTER COMMUNICATION USING
CRYPTOGRAPHY

713/155 .Central trusted authority provides computer
authentication

2 713/502 (1 OR, 1 XR)
Class 713 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: SUPPORT

713/500 CLOCK, PULSE, OR TIMING SIGNAL GENERATION OR
ANALYSIS

713/502 .Counting, scheduling, or event timing

2 714/4 (0 OR, 2 XR)
Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY

714/100 DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING

714/1 .Reliability and availability

714/2 ..Fault recovery

714/3 ...By masking or reconfiguration

714/4Of network

2 714/56 (1 OR, 1 XR)
Class 714 : ERROR DETECTION/CORRECTION AND FAULT
DETECTION/RECOVERY

714/100 DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING

714/1 .Reliability and availability

714/48 ..Error detection or notification

714/55 ...Timing error (e.g., watchdog timer time-out

10029826_CLSTITLES

714/56 Bus or I/O channel device fault

2 719/312 (2 OR, 0 XR)

Class 719 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING
SYSTEMS: INTERPROGRAM COMMUNICATION OR INTER

PROCESS

719/312 COMMUNICATION (IPC)
INTERPROGRAM COMMUNICATION USING SHARED MEMORY